

# WEST Search History

DATE: Saturday, December 11, 2004

09/776 951

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>			
<input type="checkbox"/>	L11	('20010031507'  '4876702')!.PN.	4
<input type="checkbox"/>	L10	L6 and (capacit\$4 )	6
<input type="checkbox"/>	L9	L8 not 12	0
<input type="checkbox"/>	L8	L6 and (capacit\$4 load)	1
<input type="checkbox"/>	L7	L6 and (DRAM or memory or module)	10
<input type="checkbox"/>	L6	signal near2 (delay\$3 or transit\$3 near times) with (printed circuit board or 'pcb') same measur\$3	16
<input type="checkbox"/>	L5	('6703844'  '20010031507'  'US20030034784A'  '6223334'  '20030034784')!.PN.	8
<input type="checkbox"/>	L4	('US20030034784A')!.PN.	0
<input type="checkbox"/>	L3	('6223334')!.PN.	2
<input type="checkbox"/>	L2	('20010031507')!.PN.	2
<input type="checkbox"/>	L1	signal near2 transit\$3 near times with (printed circuit board or 'pcb') same measur\$3	5

END OF SEARCH HISTORY

h e b b cg b chh e f ff e ch e g e